

Notice of References Cited	Application/Control No. 09/823,235	Applicant(s)/Patent Under Reexamination WANG ET AL.	
	Examiner Shane F Gerstl	Art Unit 2183	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-5,357,617	10-1994	Davis et al.	712/245
	C	US-4,916,652	04-1990	Schwarz et al.	708/510
	D	US-6,092,187	07-2000	Killian, Earl A.	712/239
	E	US-6,044,222	03-2000	Simons et al.	717/156
	F	US-6,594,824	07-2003	Volkonsky et al.	717/159
	G	US-5,937,195	08-1999	Ju et al.	717/156
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Ferreira de Souza, A and Rounce, P; Dynamically Scheduling the Trace Produced During Program Execution into VLIW Instructions; Parallel and Distributed Processing, 1999; Pages 248-257
	V	Tanenbaum, Andrew S; Structured Computer Organization; 1984, Prentice-Hall, Inc; 2nd Edition, Pages 10-12
	W	Jacobsen E, Rotenberg E, and Smith J E; Assigning Confidence to Conditional Branch Predictions; Microarchitecture, 1996; pages 142-152
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.